**SRILAKSHMIDARAN P**

**PROFILE**

A fresh engineering graduate seeking to contribute to the growing and demanding fields of embedded, VLSI design and computer architecture in order to collect relevant skills and help the industry grow.

**EDUCATION**

* **B.Tech Electrical and Electronics Engineering**

**CGPA – 7.82 / 10 2019-2023**

Amrita Vishwa Vidyapeetham

* **Class 12** – 79.4% **2019**

Institution:

* **Class 10** – 100% **2017**

Institution:

**TECHNICAL INTERESTS**

Embedded Systems  
VLSI Design  
Computer Architecture  
Embedded Linux  
Hardware Security

**PROJECTS**

**Range Extended Electric Vehicle (REEV)**  
· Duration/Period: 2020-2022  
· Objective: To design and build a series hybrid electric vehicle with range as the primary objective  
· Tools or techniques used: MATLAB, Ansys, Control Systems, Electrical Machines, Electrical Machines lab, SAE workshop  
· Outcome: An electric vehicle optimized for range was presented to the REEV committee for virtual evaluation

**TECHNICAL SKILLS**

MATLAB & Simulink, Rust, RISC-V Achitecture, Assembly language, C, C++, Linux kernel and OS engineering, Soft Computing, EDA tools : Xilinx Vivado, VHDL / Verilog, Python, Ansys; Blender, Ethical Haching, Reverse Engineering

**LANGUAGES**

English, Tamil

**EXTRA CURRICULAR ACTIVITIES**

**SHAKTI Microprocessor design challenge by IIT Madras**  
**·** Duration/Period: September-October 2021  
**·** Objective: To be introduced to the design flow of RISC-V microprocessor and building applications using framework provided, conducted in collaboration with NIELIT and IIT Madras, an initiative of GOI  
**·** Tools or techniques used: FPGA implementation, embedded C, compiling linux kernel from scratch**·** Outcome: Overall understanding on hardware and software integration and design of RISC-V based custom processor